Lecture Schedule

Week	Dates	Topics
1	8/24	Class introduction
	8/26	No class
2	8/29	Intro to number systems and conversion
	8/31	Number conversion, binary arithmetic
	9/2	Binary negative numbers
3	9/5	Holiday- no class
	9/7	Negative numbers, binary code
	9/9	Boolean Algebra
4	9/12	Boolean Algebra + MINTERM EXAM 1
	9/14	Boolean Algebra
	9/16	Minterms and Maxterms
5	9/19	Binary adders and subtractors
	9/21	Design using truth tables
	9/23	Design using truth tables
6	9/26	Karnaugh Maps + MINTERM EXAM 2
	9/28	Karnaugh Maps
	9/30	Multi-level gate circuits
7	10/3	Multi-level gate circuits
	10/5	Multi-level gate circuits NAND-NAND Logic
	10/7	Multi-level gate circuits: multiple output logic
8	10/10	Intro to timing diagrams, hazards in combinational logic + MINTERM EXAM 3
	10/12	Intro to timing diagrams, hazards in combinational logic
	10/14	Autumn Break- no class
9	10/17	Decoders and Encoders
	10/19	ROMS and PLAs
	10/21	PLAs

10	**10/24** **10/26** **10/28**	**Latches and Flip-Flops + MINTERM EXAM 4** **Latches and Flip-Flops** **Latches and Flip-Flops; timing diagrams**
11	**10/31** **11/2** **11/4**	**Counters** **Counters** **Registers**
12	**11/7** **11/9** **11/11**	**Analysis of clocked sequential circuits + MINTERM EXAM 5** **Finite state machines, flow diagram** **Analysis of clocked sequential circuits**
13	**11/14** **11/16** **11/18**	**Slush in case we fall behind (or new material)** **Last day of this course (lecture)**
14	**11/21**	**MINTERM EXAM 6**